ABSTRACT OF THE DISCLOSURE

The microcomputer includes a shift resistor (20) setting a potential to bus wirings (11 to 16) in a stop mode, and the shift resistor (20) can retain a setting data by inputting serially a signal for the setting data from an external data input part (60) in synchronization with external clock from an external clock input part (70). The setting data in the shift resistor (20) is supplied to the bus wirings (11 to 16) through buffers (51 to 56) in active state. The buffers (51 to 56) are in the active state when the mode signal indicates "L" (indicating a stop mode).